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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Re the Application of

Attn: OIPE

Takehiro NIITSU et al.

Application No.: 10/728,963

Docket No.: 118009

Filed: December 8, 2003

For: OPTICAL WIRING BOARD, OPTICAL BUS SYSTEM, AND METHOD OF  
MANUFACTURING OPTICAL WIRING BOARD

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records.

Respectfully submitted,

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JAO:TJP/mxm

Date: April 13, 2004

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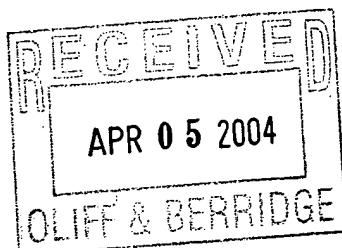
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/728,963	12/08/2003	2874	856	118009	9	16	4

CONFIRMATION NO. 9040

25944  
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ALEXANDRIA, VA 22320



FILING RECEIPT



\*OC000000012262059\*

Date Mailed: 04/02/2004

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## Assignment For Published Patent Application

FUJI XEROX CO., LTD., Tokyo, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

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Projected Publication Date: 07/15/2004

Non-Publication Request: No

Early Publication Request: No

Title

Optical wiring board, optical bus system, and method of manufacturing optical wiring board

Preliminary Class

385

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